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APPLICANT(S)	Leo Mathew et al.	GROUP ART UNIT: 2818
APPLN. NO.:	10/705,504	EXAMINER: Tu Tu V. Ho
FILED:	November 10, 2003	CONFIRMATION NO. 4770
TITLE:	INTEGRATED CIRCUIT HAVING MULTIPLE MEMORY TYPES AND METHOD OF FORMATION	

Certificate of Transmission under 37 C.F.R. 1.8

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Mail Stop ISSUE FEE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Payment of the Issue Fee of this application is concurrently made with the request
that the application issue in the name of Freescale Semiconductor, Inc. An assignment of all
rights, title and interest in the instant application from Motorola, Inc. to Freescale
Semiconductor, Inc. has been previously filed for recordation in accordance with 37 CFR
3.11, but it is not believed to have been recorded by the USPTO as of yet.

Respectfully submitted,

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